This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Original) An apparatus comprising:

an amplitude mapping circuit for converting at least a portion of an amplitude signal to a binary value; and,

a plurality of amplifiers coupled to the amplitude mapping circuit,

wherein the binary value is transmitted to at least one of the plurality of amplifiers to specify a gain level of the amplifier.

2. (Original) The apparatus of claim 1, further comprising:

a rectangular to polar converter for converting a signal into amplitude and phase portions, and for transmitting said amplitude portion to the amplitude mapping circuit.

3. (Original) The apparatus of claim 2, further comprising:

a phase modulator for modulating the phase portion with a carrier signal.

4. (Currently Amended) The apparatus of claim 2, further comprising:

a <u>modulator</u>mixer for modulating the phase portion with a carrier signal.

5. (Original) The apparatus of claim 1, further comprising:

at least one gain control source for applying the binary value to at least one of the

plurality of amplifiers.

6. (Original) The apparatus of claim 1, further comprising:

an input matching circuit coupled to the respective inputs of the plurality of amplifiers;

and

an output matching circuit coupled to the respective outputs of the plurality of amplifiers.

7. (Original) The apparatus of claim 6, further comprising:

at least one gain control source coupled to a control terminal of at least one of the

plurality of amplifiers.

8. (Original) A method for processing a signal, comprising the steps of:

separating the signal into amplitude and phase components;

generating a binary representation of at least a portion of the amplitude component; and,

specifying a gain level of one of a plurality of amplifiers in response to the generated

binary representation.